

<b>Notice of References Cited</b>	Application/Control No. 10/782,975	Applicant(s)/Patent Under Reexamination MAEMURA, KIMIHIRO	
	Examiner Mai-Huong Tran	Art Unit 2818	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,759,290	07-2004	Ogura et al.	438/200
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Owa, Nonvolatile Semiconductor Memory Device, Pub. No. US 2004/0229407 A1, Pub date Nov. 18, 2004.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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